

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	5507	(dc adj2 (bus or voltage or link)).ab.	USP AT	2006/06/ 20 08:50	
2	BRS	L2	2880 67	capacitor or capacitance	USP AT	2006/06/ 20 08:50	
3	BRS	L3	2949 5	small adj2 value	USP AT	2006/06/ 20 08:51	
4	BRS	L4	5571 1	small\$3 near3 capacit\$4	USP AT	2006/06/ 20 08:51	
5	BRS	L5	6553 31	motor	USP AT	2006/06/ 20 08:51	
6	BRS	L6	128	1 and 2 and (3 or 4) and 5	USP AT	2006/06/ 20 08:52	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	2395 20	converter	USP AT	2006/06/ 20 10:50	
2	BRS	L2	1121 33	inverter	USP AT	2006/06/ 20 10:50	
3	BRS	L3	6553 31	motor	USP AT	2006/06/ 20 10:50	
4	BRS	L4	7352 74	filter\$2	USP AT	2006/06/ 20 10:50	
5	BRS	L5	2363 4	ripple	USP AT	2006/06/ 20 10:50	
6	BRS	L6	2276 45	capacitor	USP AT	2006/06/ 20 10:51	
7	BRS	L7	561	1 and 2 and 3 and 4 and 5 and 6	USP AT	2006/06/ 20 10:51	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	143	ripple adj2 voltage.ab.	USP AT	2006/06/ 20 10:48	
2	BRS	L2	1162 6	rectif\$5.ab.	USP AT	2006/06/ 20 10:48	
3	BRS	L3	6553 31	motor	USP AT	2006/06/ 20 10:48	
4	BRS	L4	14	1 and 2 and 3	USP AT	2006/06/ 20 10:48	

	U	1	Document ID	Issue Date	Pages	Title
1			US 6486644 B1	20021126	9	Method and architecture for limiting input current to a broadband network power supply
2			US 5973936 A	19991026	6	Current-voltage regulator
3			US 5606232 A	19970225	9	DC on line AC brushless motor
4			US 5513058 A	19960430	13	DC link circuit for an electronically commutated motor
5			US 5404082 A	19950404	11	High frequency inverter with power-line-controlled frequency modulation
6			US 5353213 A	19941004	9	Circuit configuration for a self-oscillating blocking oscillator switched-mode power supply
7	X		US 5224029 A	19930629	8	Power factor and harmonic correction circuit including ac startup circuit